

10/645330

Sheet 2 of 3

PTO-1449 REPRODUCED INFORMATION DISCLOSURE CITATION IN AN APPLICATION August 21, 2003 (Use several sheets if necessary)	ATTORNEY DOCKET NO. 2037.1004-007		CONT. OF APPLICATION NO. 10/348,062	
	APPLICANT Richard C. Foss, <i>et al.</i>			
	FILING DATE		CONFIRMATION NO.	GROUP

FOREIGN PATENT DOCUMENTS						
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO	
45	AL	JP-0214094	08/1990	Japan	YES	
	AM					
	AN					
	AO					
	AP					
	AQ					
	AL2					
	AM2					
	AN2					
	AO2					
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	AQ4					
	AL5					
	AM5					

EXAMINER M. TRAN	DATE CONSIDERED 4/1/04
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PTO-1449 REPRODUCED INFORMATION DISCLOSURE CITATION IN AN APPLICATION August 21, 2003 (Use several sheets if necessary)	ATTORNEY DOCKET NO. 2037.1004-007	CONT. OF APPLICATION NO. 10/348,062	
	APPLICANT Richard C. Foss, <i>et al.</i>		
	FILING DATE	CONFIRMATION NO.	GROUP

U.S. PATENT DOCUMENTS					
EXAM- INER INI- TIAL	REF. NO.	DOCUMENT NUMBER		ISSUE DATE / PUBLICATION DATE	NAME
			<i>clas</i>	<i>sub</i>	
<i>RT</i>	AA	4,754,164	<i>307</i>	<i>269</i>	Flora et al.
	AB	5,287,319	<i>365</i>	<i>233</i>	Fukumoto
	AC	5,311,483	<i>365</i>	<i>233</i>	Takasugi
	AD	5,544,203	<i>375</i>	<i>376</i>	Casasanta et al.
<i>MT</i>	AE	5,604,775	<i>375</i>	<i>376</i>	Saitoh et al.
	AF	5,754,164	<i>327</i>	<i>284</i>	Flora
	AG	5,796,673	<i>365</i>	<i>233</i>	Foss et al.
	AH	6,067,272	<i>365</i>	<i>233</i>	Foss et al.
	AI	6,205,083	<i>365</i>	<i>233</i>	Foss et al.
<i>RM</i>	AJ	5,319,755	<i>395</i>	<i>325</i>	Farmwald et al.
	AK	5,223,755	<i>307</i>	<i>603</i>	Richley
	AA2	5,553,276	<i>395</i>	<i>550</i>	Dean
<i>an</i>	AB2	5,440,515	<i>365</i>	<i>194</i>	Chang et al.
	AC2				
	AD2				
	AE2				
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	AH2				
	AI2				
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	AK2				
	AA3				
	AB3				
	AC3				

EXAMINER <i>M. TRA</i>	DATE CONSIDERED <i>4/2/04</i>
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PTO-1449 REPRODUCED INFORMATION DISCLOSURE CITATION IN AN APPLICATION August 21, 2003 (Use several sheets if necessary)	ATTORNEY DOCKET NO. 2037.1004-007		CONT. OF APPLICATION NO. 10/348,062	
	APPLICANT Richard C. Foss, <i>et al.</i>			
	FILING DATE	CONFIRMATION NO.	GROUP	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
MT	AR	Nakamura, Kazuyuki, et al., "A 220-MHz Pipelined 16-Mb BiCMOS SRAM with PLL Proportional Self-Timing Generator," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 29, No. 11, pp. 1317-1322(November 1994).
MT	AS	Kushiyama, N., et al., "500 Mbyte/sec Data-Rate 512 Kbits x 9 DRAM Using a Novel I/O Interface," 1992 Symposium on VLSI Circuits Digest of Technical Papers, pp. 66-67 (1992).
MT	AT	Kushiyama, Natsuki, et al., "A 500-Megabyte/s Data-Rate 4.5M DRAM," <i>IEEE Journal of Solid-State Circuits</i> , Vol 28, No. 4, pp. 490-498 (April 1993).
M	AU	Nakamura, Kazuyuki, et al., "A 220MHz Pipelined 16Mb BiCMOS SRAM with PLL Proportional Self-Timing Generator," IEEE International Solid-State Circuits Conference, Session 15, pp. 258-259, 200-201 & 312(February 18, 1994)
	AV	
	AW	
	AX	
	AY	
	AZ	
	AR2	
	AS2	
	AT2	
	AU2	
	AV2	
	AW2	

EXAMINER M. TRAN	DATE CONSIDERED 4/1/04
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